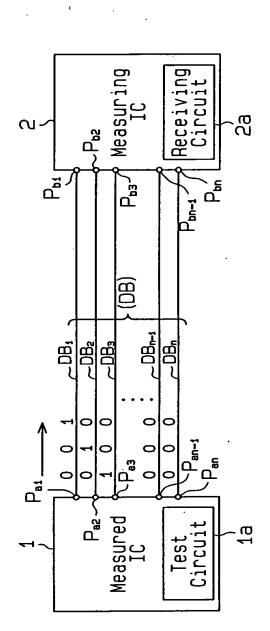
O.G. FIG.
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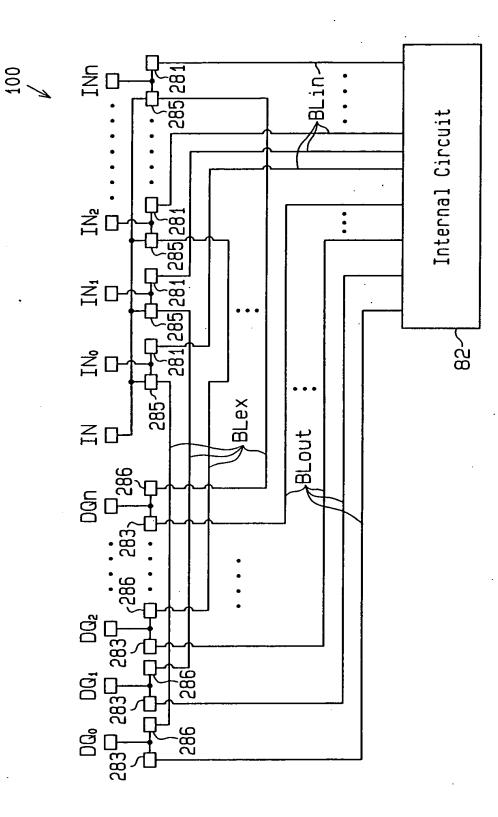
Fig.1 (Prior



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Fig.2(Prior Art)



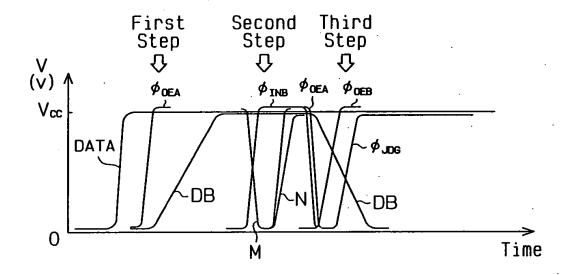
APPROVED O.G. FIG.

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Fig.4



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Memory (Measured 38 ..31 ខ ខ 36 30a/ P_{b1} (P_{b2}~P_{bn}) Fig.5 .P₈₁ (P₈₂~P₈₁) ~10a 24 16 / 6ND / ည် ġ 14 27 12a -/1 21-23-12 18 Ø OEA 53 DATA-26-7 CPU (Measaring]

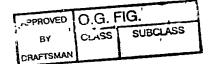
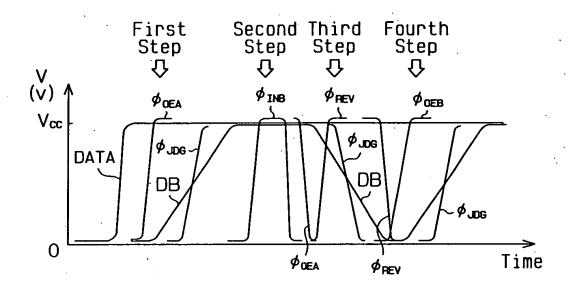
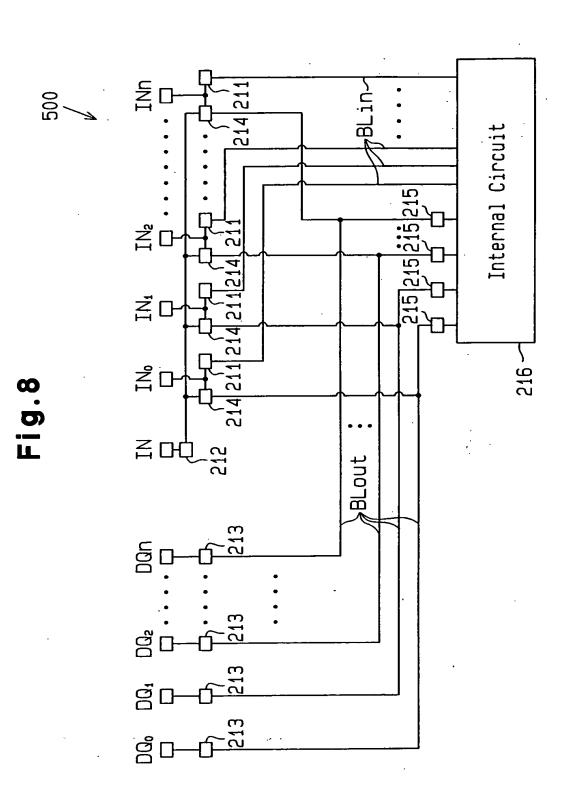
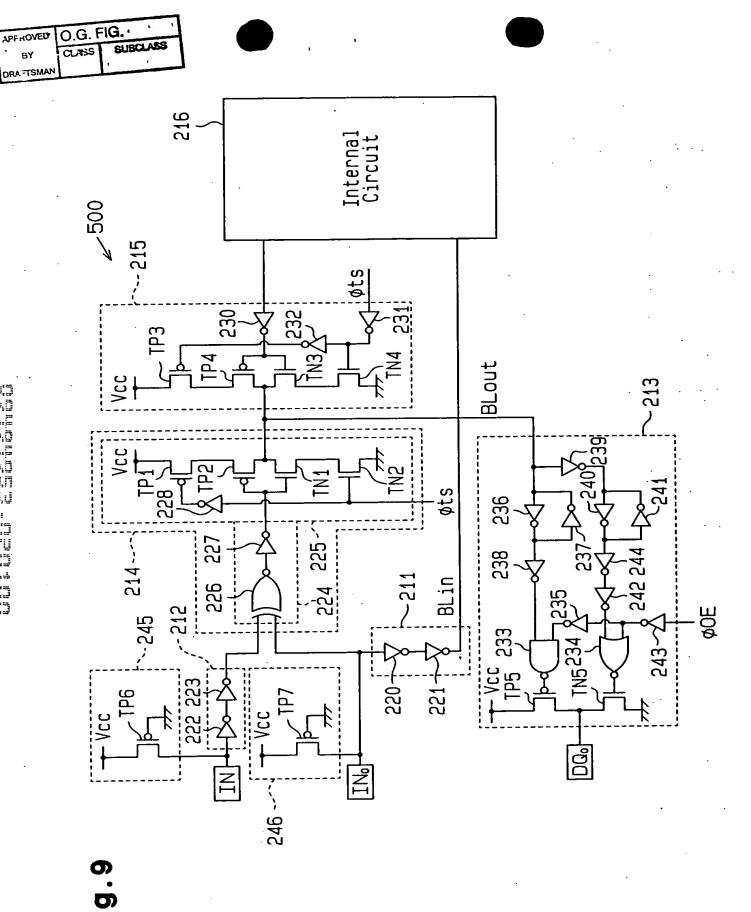


Fig.6



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Fig.11

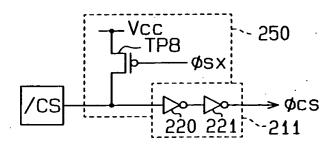


Fig.12

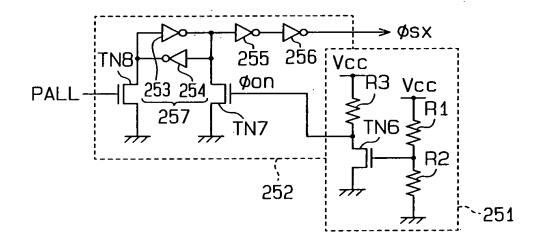


Fig.13

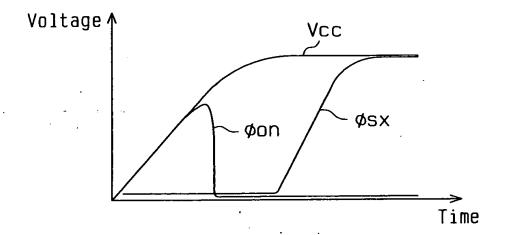


Fig.14

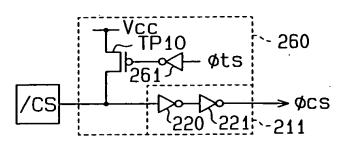


Fig.15

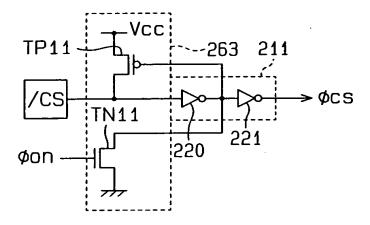


Fig.16

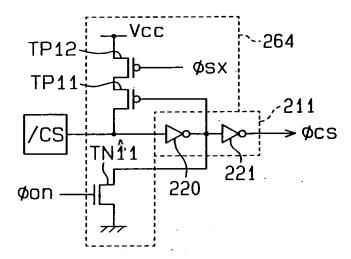


Fig.17

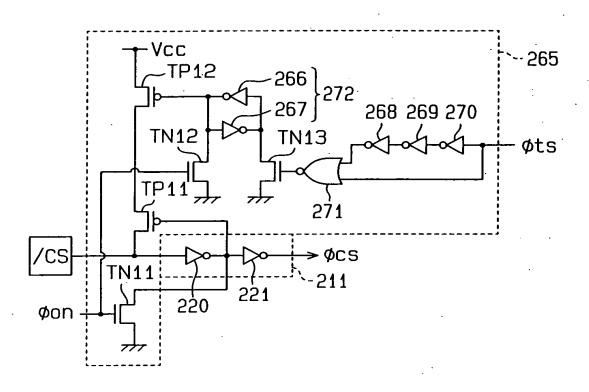
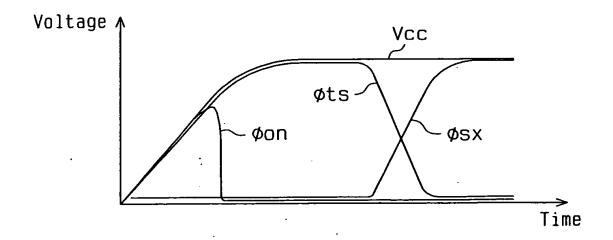


Fig.18



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Fig.19

